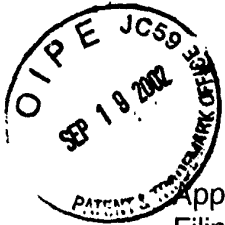


●EV077335408



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#8/103
supple
10/14/02

Application Serial No. 10/056,179
Filing Date January 22, 2002
Inventor Theodore M. Taylor
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner Howard Weiss
Attorney's Docket No. MI22-1824
Title: Floating Gate Transistors and Methods of Forming Floating Gate Transistors

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patent listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: Sept. 19, 2002

Frederick M. Fliegel, Ph.D.
Reg. No. 36,138
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828
(509) 624-4276

09/25/2002 AOSHAM1 00000047 10056179

01 FC:126

180.00 OP

RECEIVED
SEP 25 2002
TECHNOLOGY CENTER 2800